

# SVC compensation on a wide-area control in large electric power systems for mitigating small-signal instability

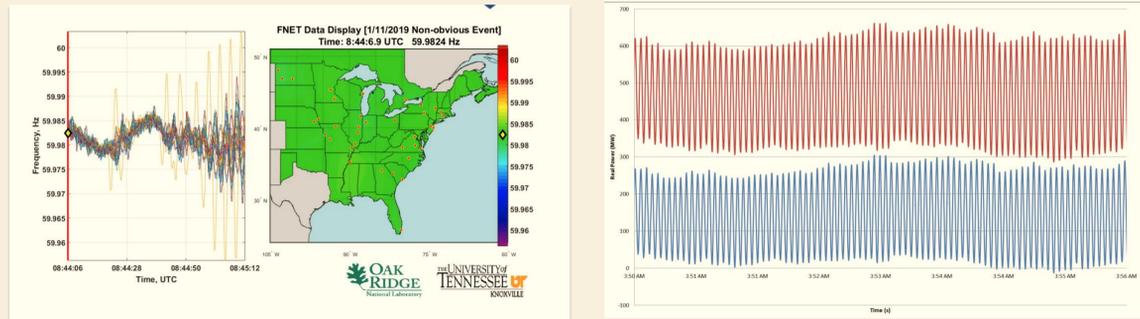
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## 1. Background

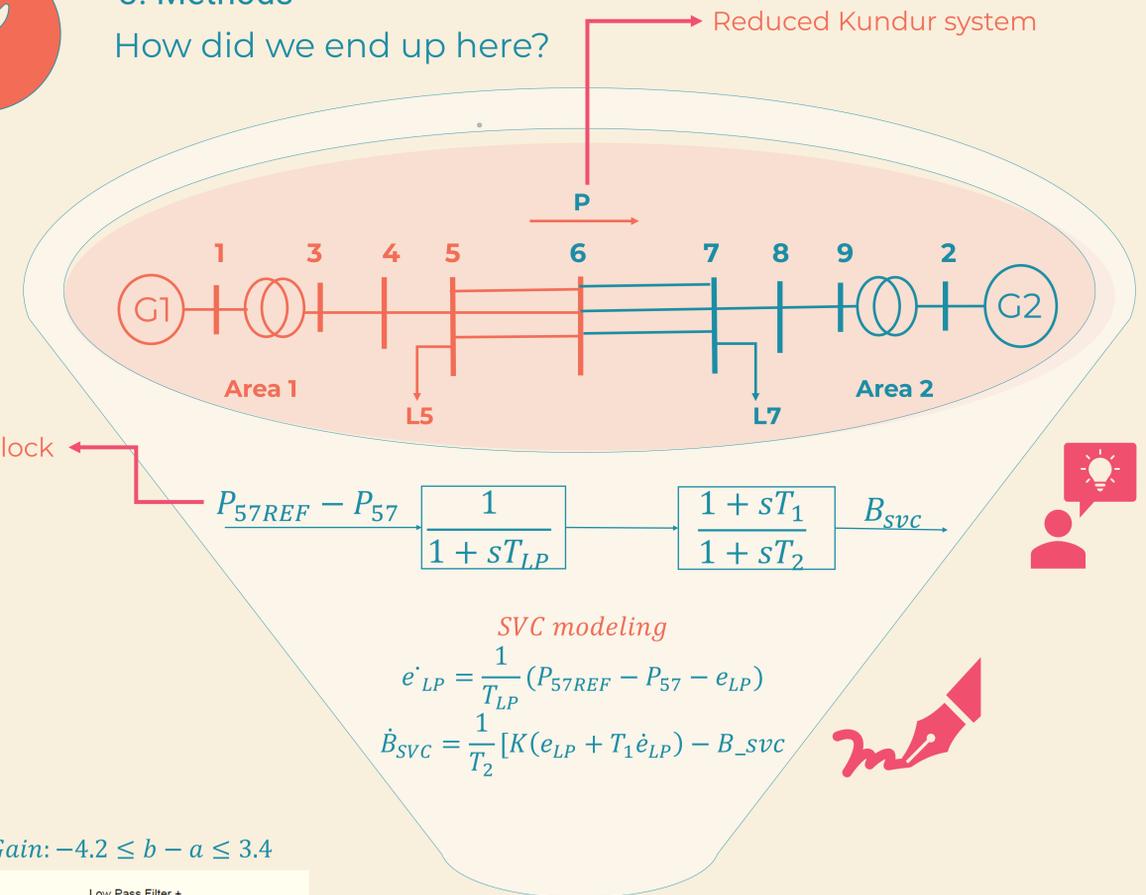


- Events might excite inter-area modes leading to potential wide-area oscillations
- Effect on power system, its component and customer
- Prevention by having an inter-area mode with high damping.



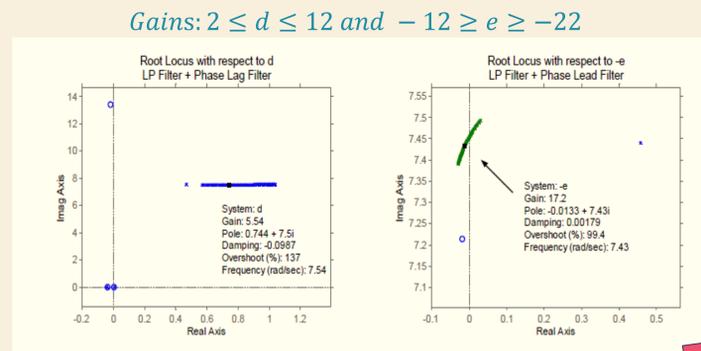
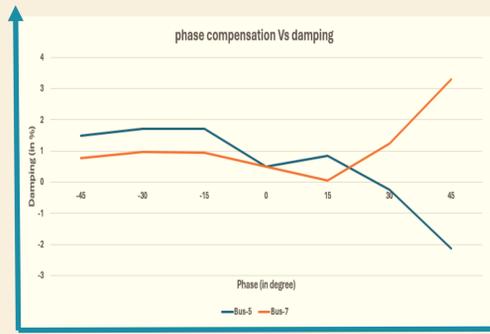
## 3. Methods

How did we end up here?



## 2. Main findings

SVC to work on full PSDC mode to increase the damping of inter-area mode by phase compensation and sending/receiving end of the tie-line in the power system.

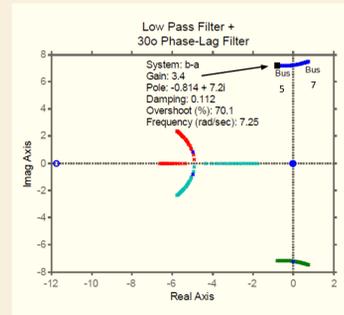


## 4. Conclusion

How should we interpret this information?

- Sending side of tie-line: SVC PSDC should be phase lag compensator
- Receiving side of tie-line: SVC PSDC should be phase lead compensator<sup>[2]</sup>

Gain:  $-4.2 \leq b - a \leq 3.4$



Root locus for SVC @ Bus-5 and Bus-7<sup>[2]</sup>

$$J = \begin{bmatrix} 0 & 1 & 0 & 0 & 0 & 0 \\ J_{21} & -\frac{K_{D1}}{2H_1} & J_{23} & 0 & 0 & a \\ 0 & 0 & 0 & 1 & 0 & 0 \\ J_{41} & 0 & J_{43} & -\frac{K_{D2}}{2H_2} & 0 & b \\ J_{51} & 0 & J_{53} & 0 & -\frac{1}{T_{2LP}} & c \\ J_{61} & 0 & J_{63} & 0 & d & e \end{bmatrix}$$

Jacobian Matrix<sup>[2]</sup>

\*Note: SVC : Static Var Compensator, PSDC: Power Swing Damping Control, EI : Eastern Interconnection  
\*\*This work was done by Jaime Quintero while he was student at Washington State University

